

# EUROPEAN PATENT OFFICE

## Patent Abstracts of Japan

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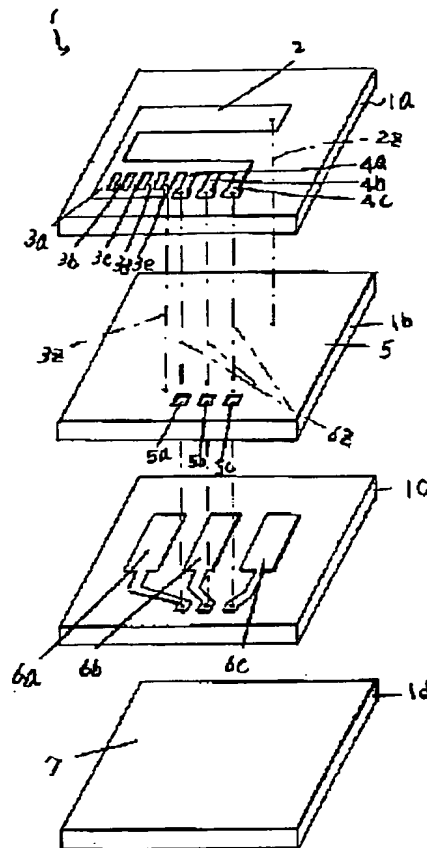
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TITLE : RESONANCE CIRCUIT SUBSTRATE



**ABSTRACT :** PURPOSE: To provide a resonance circuit substrate which can reduce the size of its substrate, can increase its frequency adjusting range, and also can be optionally undergo its rough/fine adjustment.

**CONSTITUTION:** A multilayer circuit substrate 1 includes the dielectric layers 1a-1d, a ground conductor layer 5 formed between the 1st layers 1a and 1b, and the capacitive electrode patterns 6a-6c which are formed between the 2nd layers 1b and 1c against the layer 5. Then a microstrip conductor 2 serving as an inductance generating conductor is provided on the surface of the substrate 1 together with the stub conductors 3a-3e which adjusts the line length of the conductor 2, and the trimming conductors 4a-4c which are connected to the conductor 2 and the patterns 6a-6c.

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